

Shubhabrata Sengupta

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Objective

I am looking for a research oriented role in the industry which allows me to pursue my research interests in parallel data structures and graphics.

Education

Ph.D in Computer Science, Expected March 2010
University of California, Davis, CA, USA
GPA: 4.00/4.00

M.Sc in Mathematics, June 1998
Indian Institute of Technology, Kharagpur, WB, India
GPA: 8.3/10.0

B.Sc in Mathematics, June 1996
Indian Institute of Technology, Kharagpur, WB, India
GPA: 8.18/10.0

Research

Department of Computer Science Davis, CA, USA
University of California Davis
Research Assistant 2005–2009
Working on developing general-purpose algorithms for upcoming graphics architectures. Current research interests are parallel prefix-sum and its variants, developing fast parallel sort, and building sparse data structures on data parallel hardware. I am one of the lead developers of the data parallel primitives library, CUDPP.
Developed new rendering techniques and parallel algorithms on graphics processors, that currently enable interactive film preview and will be feasible for games in coming years. This resulted in a robust algorithm to generate alias-free hard shadows for dynamic scenes at interactive rates.

Teaching

University of California Davis Davis, CA, USA
Teaching Assistant 2004–2005
ECS 15: Introduction to Computers. Dr. Nick Puketza.

Employment

Microsoft Research Redmond, WA, USA
Intern Jun, 2008–Aug, 2008

I was an intern in the Research group working with Hugues Hoppe on building tree based data structures on graphics processors.

NVIDIA Corporation Santa Clara, CA, USA
Intern Jun, 2007–Sep, 2007: Jan, 2008–May, 2008

I was an intern in the Research group working with Michael Garland on data-parallel programming primitives. My work over summer involved developing a data-parallel algorithm to efficiently build a specific spatial hierarchy on graphics processors. I also developed multiple segmented scan algorithms.

Pixar Animation Studios Emeryville, CA, USA
Summer Intern Jun, 2006–Oct, 2006

I contributed to enhancing the shading quality of Pixar’s hardware rendering pipeline to closely match that of the offline rendering process in many cases. The technique involves compactly storing and accessing sparse volumetric data on graphics processors.

Sun Microsystems Bangalore, India
Member of Technical Staff 2000–2004

Member of the development team working on SunONE Application Server.

HCL Technologies Broomfield, CO, USA
Tokyo, Japan

Member of Technical Staff 1998–2000

Member of the development team working on REELs tape library management software at StorageTek.

Involved in designing a large online betting system at NTT Japan.

Papers

D. A. Alcantara, A. Sharf, F. Abbasinejad, S. Sengupta, M. Mitzenmacher, J. D. Owens, N. Amenta “Real-time Parallel Hashing on the GPU” ACM Transactions on Graphics (Proceedings of ACM SIGGRAPH Asia 2009)

C. Lauterbach, M. Garland, S. Sengupta, D. Luebke, D. Manocha. “Fast BVH construction on GPUs” Proc. of Eurographics 2009

S. Sengupta, M. Harris, Y. Zhang, J. D. Owens “Scan Primitives for GPU computing” Graphics Hardware 2007

A. E. Lefohn, S. Sengupta, J. D. Owens “Resolution Matched Shadow Maps” ACM Transactions on Graphics, 2007

S. Sengupta, A. E. Lefohn, J. D. Owens. “A Work-Efficient Step-Efficient Prefix Sum Algorithm” Proceedings of the 2006 Workshop on Edge Computing using New Commodity Architectures.

A. E. Lefohn, J. M. Kniss, R. Strzodka, S. Sengupta, J. D. Owens. “Glif: Generic Efficient, Random-Access GPU data structures. ACM Transactions on Graphics, January 2006.

SIGGRAPH Sketches

A. E. Lefohn, S. Sengupta, J. M. Kniss, R. Strzodka and J. D. Owens. “Dynamic Adaptive Shadow Maps on Graphics Hardware”. Technical Sketches Program, ACM SIGGRAPH 2005, August 2005.

J. M. Kniss, A. E. Lefohn, R. Strzodka, S. Sengupta and J. D. Owens. “Octree Textures on Graphics Hardware”. Technical Sketches Program, ACM SIGGRAPH 2005, August 2005.

Book Chapters

M. Harris, S. Sengupta, J. D. Owens “Parallel Prefix Sum (Scan) with CUDA” GPU Gems 3, chapter 39, Addison Wesley

Technical Reports

S. Sengupta, M. Harris, and M. Garland. “Efficient parallel scan algorithms for GPUs.” NVIDIA Technical Report NVR-2008-003, December 2008

J. D. Owens, S. Sengupta and D. Horn. “Assessment of Graphics Processing Units (GPUs) for Department of Defense (DoD) Digital Signal Processing (DSP) Applications”. Technical Report ECE-CE-2005-3, Computer Engineering Research Laboratory, University of California, Davis, 2005.

Patents

Four filed through NVIDIA Corporation. Details upon request.

Open source involvement

One of the lead developers of CUDA Data Parallel Primitives (<http://code.google.com/p/cudpp/>) library.

Fellowships and Awards

- Best Graduate Student Researcher Award, Department of Computer Science, UC-Davis
- Best Paper Award, Graphics Hardware 2007.
- NVIDIA Fellowship, 2007-2008.
- NVIDIA Fellowship, 2008-2009.
- Departmental fellowship, Department of Computer Science, University of California Davis, 2004–2005.
- Outstanding Achievement Award, Sun Microsystems, 2001.
- Outstanding Achievement Award, Sun Microsystems, 2002.

Computer Skills

- Languages: CUDA, C, Cg, C++
- Operating Systems: Almost all flavors of Unix and Windows

Miscellaneous

Indian citizen on F-1 status. Looking for a full time position upon graduation.